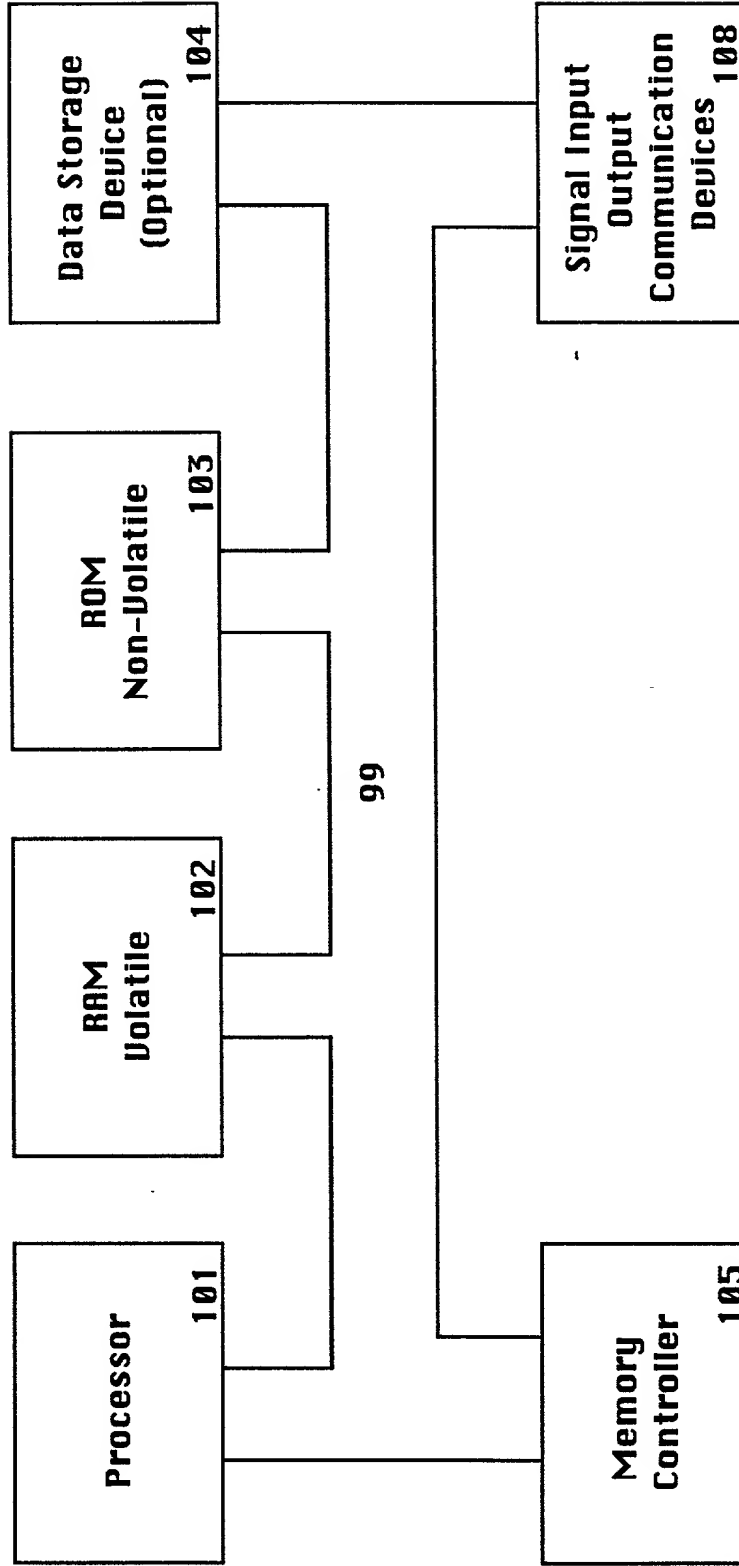


100



**Fig. 1**

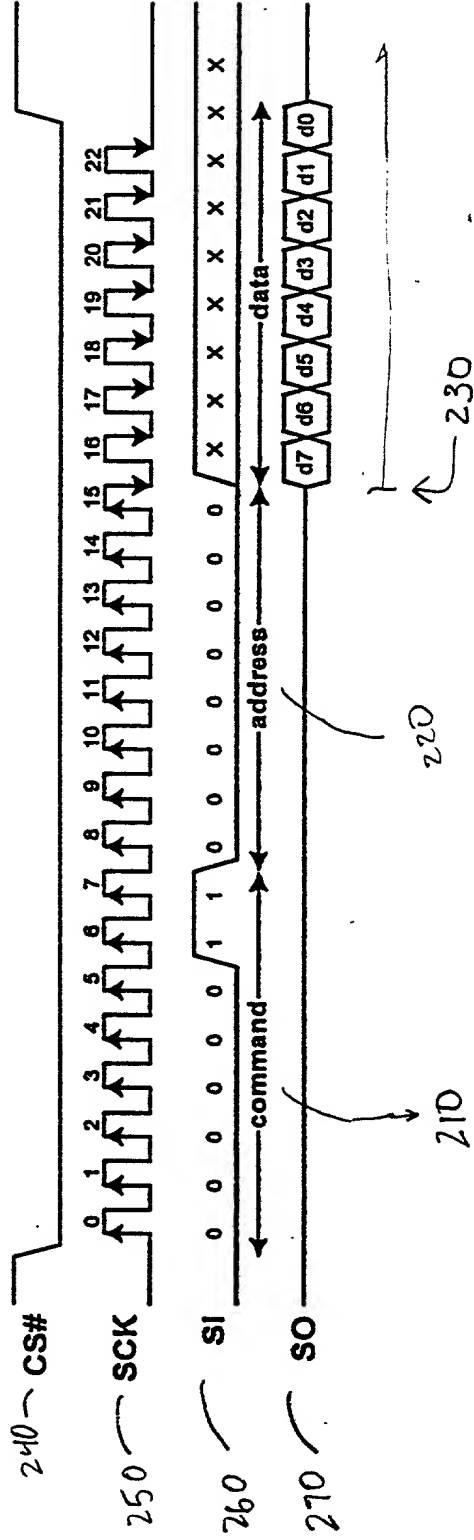


Fig. 2

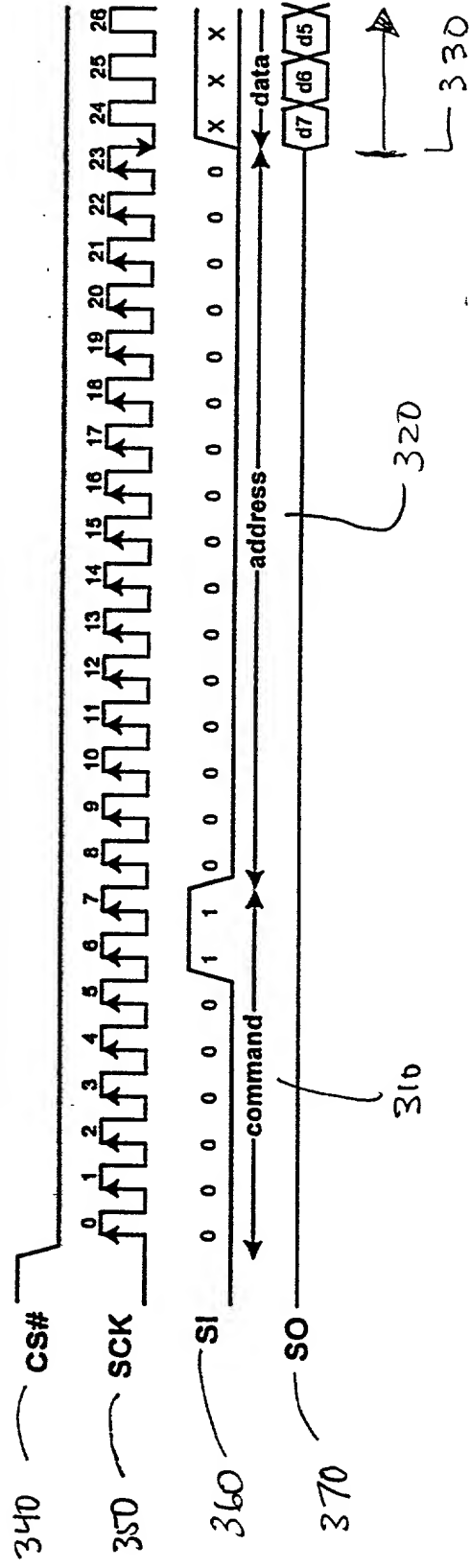
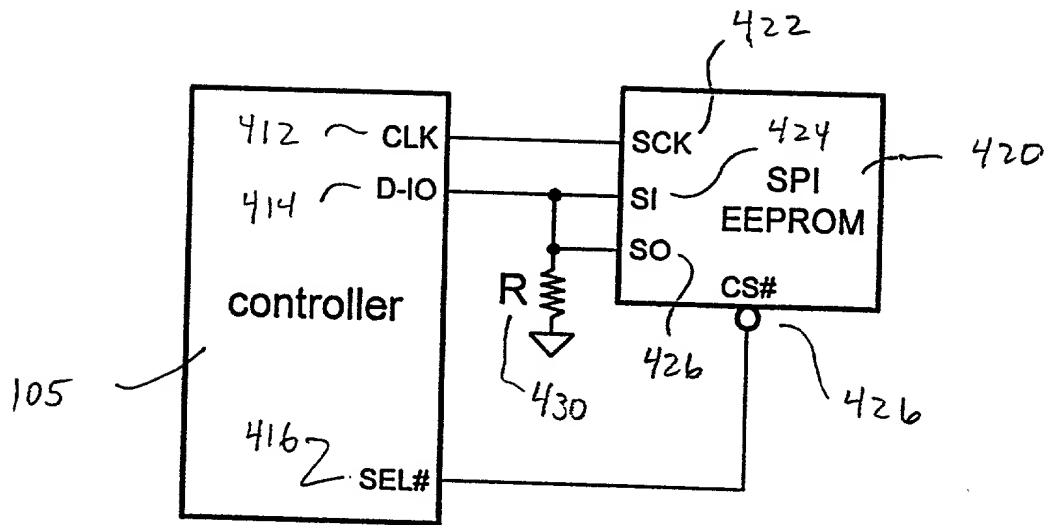


Fig. 3



**Fig. 4**

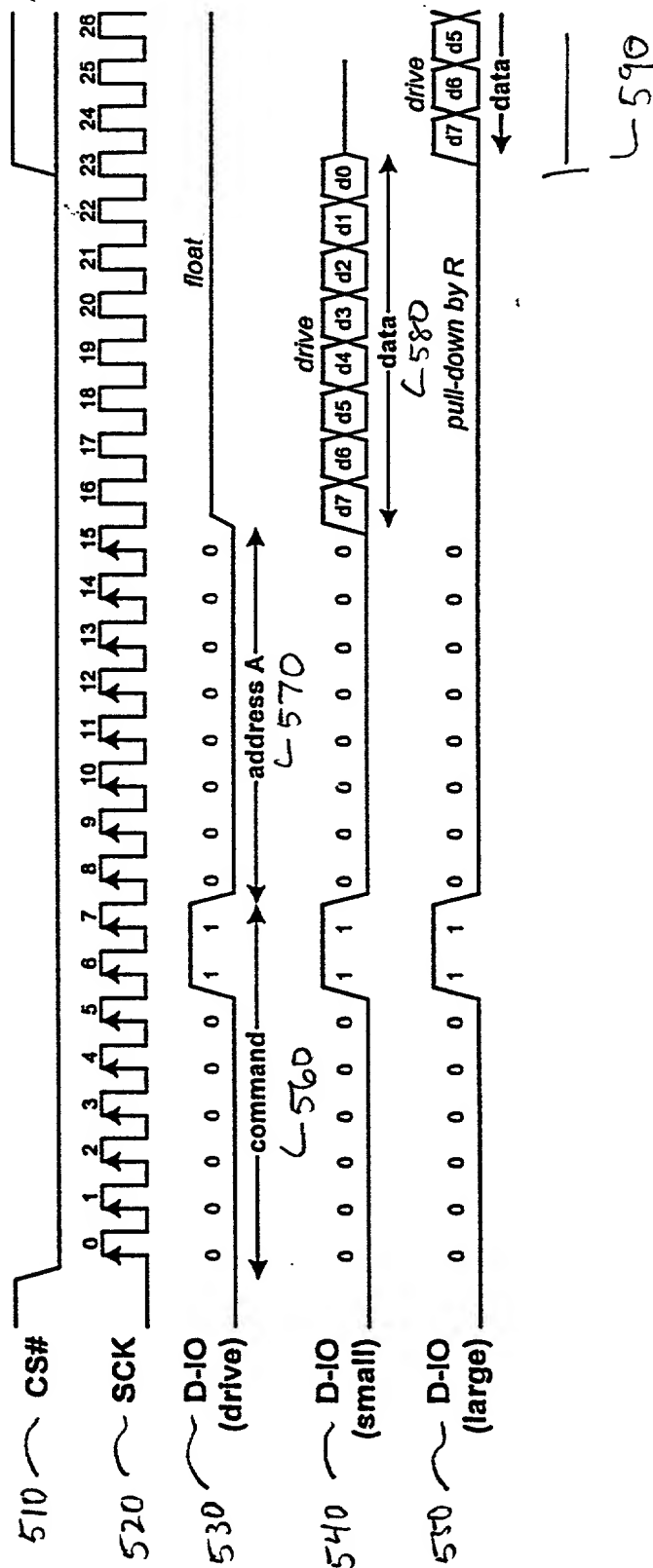


Fig. 5

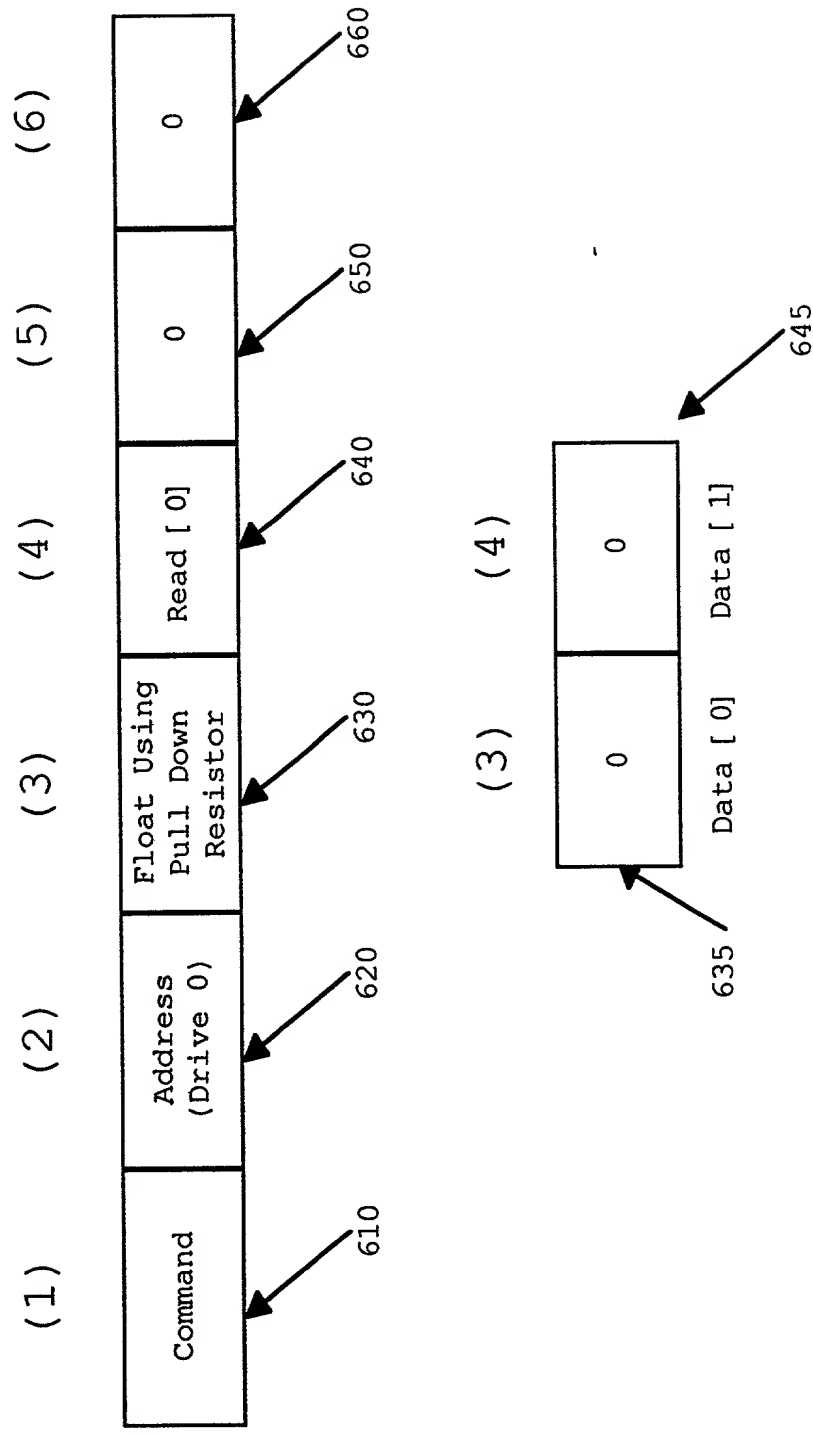
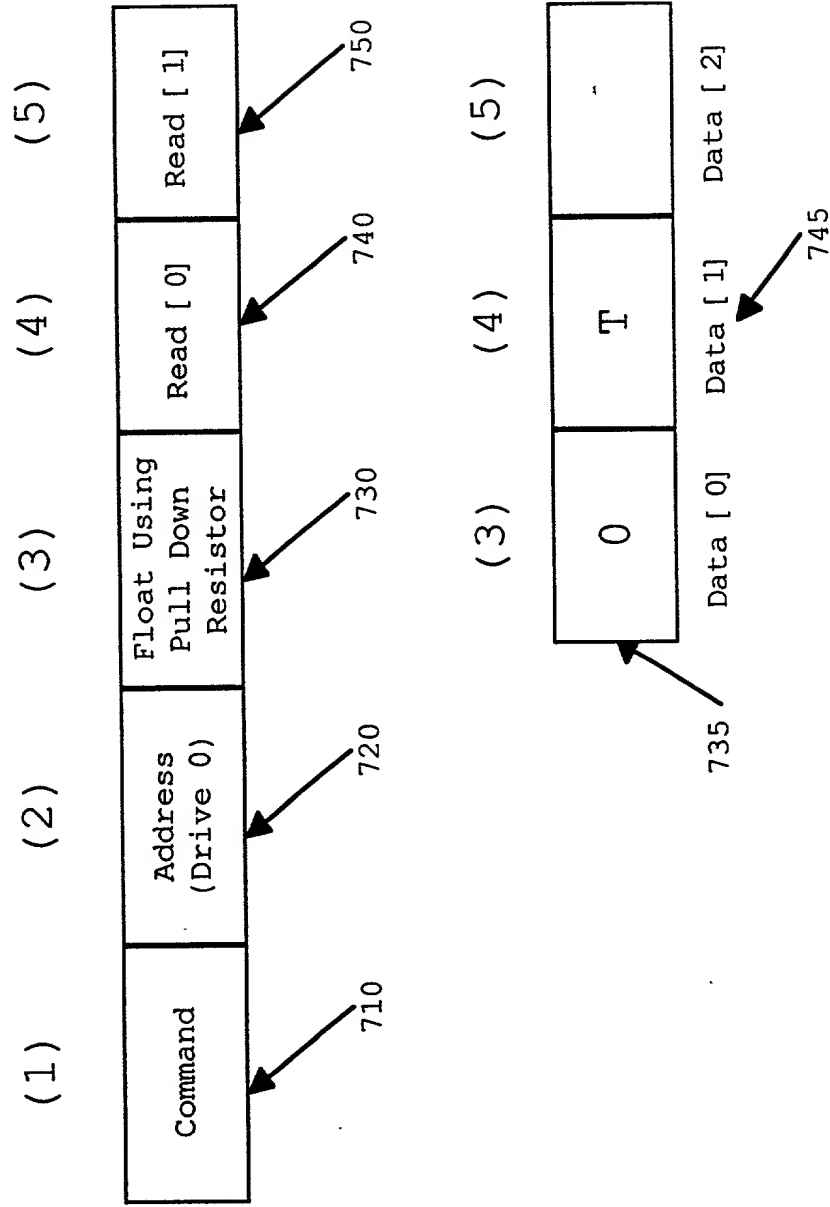
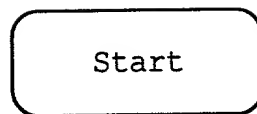


Fig. 6



**Fig. 7**



800

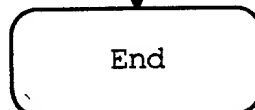
Send the READ Command Byte. 810

Drive the Data Input/Output Pin Low for Eight Serial Clock Pulses. 820

Float the Data Input/Output Pin. 830

Weakly Pull Down the Data Input/Output Pin to a Logic "0" Level. 840

Detect the Presence of Zero or Non-Zero Byte Values at the Data Input/Output Pin of the Controller to Determine the Presence of a Small, Large, or Larger SPI EEPROM, or Alternatively, the Absence of an SPI EEPROM. 850



**Fig. 8**